

100-15 U.S. PTO  
09/618971

	Subclass
Class	
ISSUE CLASSIFICATION	

PATENT NUMBER

## U.S. UTILITY Patent Application

*O.I.P.E.*  
SCANNED *M.G.* Q.A. *G.H.*

PATENT DATE

APPLICATION NO. 09/618971	CONT/PRIOR	CLASS 326 703	SUBCLASS 28	ART UNIT 2819 2123	EXAMINER <i>THANGAVELU</i>
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APPLICANTS  
Vikram Gupta

Title Method for designing mixed signal integrated circuits and  
configurable synchronous digital noise emulator circuit

PTO-2040  
12/99

### ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
<b>INTERNATIONAL CLASSIFICATION</b>					
<input type="checkbox"/> Continued on Issue Slip Inside File Jacket					

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	(Assistant Examiner) _____ (Date) _____			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____	(Primary Examiner) _____ (Date) _____			ISSUE FEE	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	(Legal Instruments Examiner) _____ (Date) _____			Amount Due	Date Paid
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